



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Te Patent Application of:

Kei MURAYAMA

Application No.: 10/603,812

Confirmation No.: 2774

Filed: June 26, 2003

U.S. Patent No.: 6,835,597

Issued: December 28, 2004

For:

SEMICONDUCTOR PACKAGE

**ATTENTION** 

CERTIFICATE OF CORRECTION

BRANCH

Batch No.:

Certificate

MAY 1 8 2005

of Correction

## REVISED REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

This Revised Request corrects the cited error at col. 1, line 62 which was presented incorrectly in the Request filed May 10, 2005, to be replaced hereby.

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO: 6,835,597

DATED: December 28, 2004

INVENTOR(S): Kei MURAYAMA

Col. 1, line 3, insert -now U.S. Patent 6,803,664, issued October 12, 2004.--.

line 45, change "formed, from" to --formed from--.

line 47, change ", are" to --are--.

line 55, change "demanded," to --demanded--.

line 62, change "more," to --more--.

Col. 7, line 34, change "resist-layer" to --resist layer--.

Col. 9, line 18, change "(BGAS)" to --(BGAs)--.

Col. 10, line 41, change "Insulating" to --insulating--.

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PATENT NO. 6,835,597

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